

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
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| L1 | 187 | (real-time adj emulation) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:26 |
| L2 | 33 | 1 and (simulation with clock) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:32 |
| L3 | 287 | (simulation adj clock) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:32 |
| L4 | 78 | 3 and (real-time) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:32 |
| L5 | 6 | 4 and API | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:36 |
| L6 | 72 | 4 not 5 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 10:36 |
| L7 | 17 | ("5375074" "5440719" "5794128" "5828867" "5832272" "5850538" "5956261").PN. OR ("6134514").URPN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2007/02/27 10:46 |

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| L8 | 21 | ("4669113" "4792913" "4979118" "5197127" "5317525" "5440719" "5481484" "5537468" "5561841" "5583792" "5701439" "5794005" "5809282" "5828867" "5881237" "6081843" "6134514" "6393386" "6442141" "6466925").PN. OR ("6820042").URPN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2007/02/27 11:15 |
| L9 | 18 | (opnet and technologies).as. | US-PGPUB; USPAT; USOCR | OR | ON | 2007/02/27 11:40 |
| L10 | 3315 | (event with clock) and real-time | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/27 11:41 |
| L11 | 202 | (event with (call adj back)) and real-time | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/27 11:41 |
| L12 | 0 | 11 and (real-time adj simulator) | USPAT | OR | OFF | 2007/02/27 11:42 |
| L13 | 0 | 11 and (real-time with simulator) | USPAT | OR | OFF | 2007/02/27 11:42 |
| L14 | 0 | 11 and (real-time with simulat\$5) | USPAT | OR | OFF | 2007/02/27 11:42 |
| L15 | 25 | 11 and real-time and simulat\$5 | USPAT | OR | ON | 2007/02/27 11:42 |

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| S1 | 1 | ("6820042").PN. | USPAT | OR | OFF | 2007/02/27 12:55 |
| S2 | 0 | S1 and (simulation adj clock) | USPAT | OR | OFF | 2007/02/27 12:54 |
| S3 | 0 | S1 and (simulation with clock) | USPAT | OR | OFF | 2007/02/27 12:54 |
| S4 | 65 | continuous and (real-time or (real adj time)) and simulator and synchronization and clock and event and advancing | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/27 12:57 |

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1 Duration calculus in COOZ

Xiaodong Yuan, Jiajun Chen, Guoliang Zheng

May 1998 **ACM SIGSOFT Software Engineering Notes**, Volume 23 Issue 3

Publisher: ACM Press

Full text available: [pdf\(106.53 KB\)](#) Additional Information: [full citation](#), [index terms](#)



2 Microarchitectural techniques for power reduction: Improved clock-gating through

transparent pipelining

Hans M. Jacobson

August 2004 **Proceedings of the 2004 international symposium on Low power electronics and design ISLPED '04**

Publisher: ACM Press

Full text available: [pdf\(97.40 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)



This paper re-examines the well established clocking principles of pipelines. It is observed that clock gating techniques that have long been assumed optimal in reality produce a significant amount of redundant clock pulses. The paper presents a new theory for optimal clocking of synchronous pipelines, presents practical implementations and evaluates the clock power benefits on a multiply/add-accumulate unit design. Transistor level simulations show that dynamic clock power dissipation can be re ...

Keywords: adaptive pipeline depth, circuits, clock gating, dynamic pipeline scaling, high performance, low power, microarchitecture, optimal pipeline clocking, pipeline stage unification, transparent pipeline

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1 Looking ahead of real time in Hybrid component networks

Kaixin Xu, Mineo Takai, Jay Martin, Rajive Bagrodia

May 2001 **Proceedings of the fifteenth workshop on Parallel and distributed simulation PADS '01**

Publisher: IEEE Computer Society

Full text available: [pdf\(677.85 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

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While network simulation is a widely used technique for developing, testing and evaluating network protocols and applications, it is difficult and time-consuming to develop detailed models of the entire protocol stack and the application that is being evaluated. Hybrid component networks combine simulation models of protocols and applications with physical implementations of other protocols and/or applications, which can alleviate modeling complexity and build accurate models. However, hyb ...

2 Requirements for transitioning business process simulation models to real-time

operational systems

Peter Floss

December 1997 **Proceedings of the 29th conference on Winter simulation WSC '97**

Publisher: ACM Press, IEEE Computer Society

Full text available: [pdf\(475.84 KB\)](#) Additional Information: [full citation](#), [references](#), [index](#) [terms](#)

3 Simulation-based real-time scheduling: review of recent developments

Catherine M. Harmonosky

December 1995 **Proceedings of the 27th conference on Winter simulation WSC '95**

Publisher: ACM Press, IEEE Computer Society

Full text available: [pdf\(606.30 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#) [terms](#)

With increased usage of computer monitoring of the factory has come increased industrial and academic interest in developing more science-based real-time scheduling techniques. Simulation, best known for its application during system design or modification, is also being considered for use on day-to-day scheduling problems. This paper reviews recent developments using simulation as a tool in real-time scheduling.

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simulation author:kubischta

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Measurement of the decay rate and form factor parameter a in - group of 4 »

... , G Govi, P Grafstrom, W **Kubischta**, A Lacourt, S ... - Physics Letters B, 1999 - na48.web.cern.ch

Page 1. 15 July 1999 Ź . Physics Letters B 458 1999 553–563 Measurement of the decay rate and form factor parameter a in) K the decay K TMe q e y g L ...

Cited by 21 - Related Articles - [View as HTML](#) - [Web Search](#)

A precise measurement of the direct CP violation parameter Re (ε/ε) - group of 14 »

... , G Govi, P Grafstrom, W **Kubischta**, A Lacourt, M ... - Eur. Phys. J. C, 2001 - publish.edpsciences.org

... remaining differences in beam divergences and beam geometries are corrected us- ing Monte Carlo **simulation**. The small difference ...

Cited by 105 - Related Articles - [Web Search](#) - [BL Direct](#)

Wireless network emulation for distributed processing systems - group of 2 »

RJ Wellington, MD **Kubischta** - Military Communications Conference, 2003. MILCOM 2003. IEEE, 2003 - ieeexplore.ieee.org

... With compatible **simulations** and emulations, system development can progress naturally through functional design, system **simulation**, node prototyping, and ...

Cited by 2 - Related Articles - [Web Search](#) - [BL Direct](#)

A measurement of the transverse polarization of \$\\Lambda\$-hyperons produced in inelastic pN- ... - group of 10 »

... A Gonidec, P Grafstrom, W **Kubischta**, V Marzulli, G ... - Eur. Phys. J. C, 1999 - publish.edpsciences.org

... In this **simulation** exactly two reconstructed tracks hit the hodoscope in two diagonally opposite quadrants and the energy in the calorimeter was greater than ...

Cited by 7 - Related Articles - [Web Search](#) - [BL Direct](#)

[PS] Search for the decay K S → π

... , G Govi, P Grafström, W **Kubischta**, A Lacourt, M ... - Physics Letters B, 2001 - hep.fi.infn.it

... channel. A detector **simulation** based on Geant 3.21 [7] was used throughout this analysis. 3. Data analysis 3.1. Event selection ...

Cited by 3 - Related Articles - [View as HTML](#) - [Web Search](#)

Measurement of K 0 e3 form factors - group of 3 »

... , G Govi, P Grafstrom, W **Kubischta**, A Lacourt, A ... - Arxiv preprint hep-ex/0410065, 2004 - arxiv.org

... A more detailed description of the NA48 setup can be found elsewhere [11]. 3 Data processing and Monte Carlo **simulation** 3.1 Trigger and data taking ...

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... , B Gorini, P Grafstrom, W **Kubischta**, A Lacourt, I ... - Arxiv preprint hep-ex/0408053, 2004 - arxiv.org

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1 Priority assignment in real-time active databases

Rajendran M. Sivasankaran, John A. Stankovic, Don Towsley, Bhaskar Purimetla, Krithi Ramamirtham

January 1996 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 5 Issue 1

Publisher: Springer-Verlag New York, Inc.

Full text available: [pdf\(634.63 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Active databases and real-time databases have been important areas of research in the recent past. It has been recognized that many benefits can be gained by integrating real-time and active database technologies. However, not much work has been done in the area of transaction processing in real-time active databases. This paper deals with an important aspect of transaction processing in real-time active databases, namely the problem of assigning priorities to transactions. In these systems, tim ...

Keywords: Active databases - Coupling mode - Deadlines - ECA - Priority assignment - Real-time databases

2 Investigation of non-interference real-time debugging through bus cycle simulation and interpretation

T. R. Cahall, K. Y. Fang

December 1987 **Proceedings of the 1987 Fall Joint Computer Conference on Exploring technology: today and tomorrow ACM '87**

Publisher: IEEE Computer Society Press

Full text available: [pdf\(777.10 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In the recent decade, software development and debugging advancements have been greatly outpaced by the advancements in hardware design and integration. The largest portion of time spent in software development is testing and debugging. Symbolic debuggers, syntax directed editors, and high level language oriented breakpoint systems have helped reduce the time spent by software developers in debugging their programs. This is not the case for timing and load sensitive real-time software. Conv ...

3 Invited papers: ACROE - ICA: artistic creation and computer interactive multisensory simulation force feedback gesture transducers

Claude Cadoz, Annie Luciani, Jean-Loup Florens, Nicolas Castagné

May 2003 **Proceedings of the 2003 conference on New interfaces for musical**

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Jingao Wang; Qing-An Zeng; Agrawal, D.P.;
[Mobile Computing, IEEE Transactions on](#)
Volume 2, Issue 1, Jan.-March 2003 Page(s):65 - 75
Digital Object Identifier 10.1109/TMC.2003.1195152

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IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

2. **Quality-of-Service degradation strategies in multimedia wireless network**
Sen, S.; Jawanda, J.; Basu, K.; Das, S.;
[Vehicular Technology Conference, 1998. VTC 98. 48th IEEE](#)
Volume 3, 18-21 May 1998 Page(s):1884 - 1888 vol.3
Digital Object Identifier 10.1109/VETEC.1998.686083
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3. **Propulsion directorate/control and engine health management (CEHM): requirements and engine simulation**
Curry, T.; Behbahani, A.;
[Aerospace Conference, 2004. Proceedings. 2004 IEEE](#)
Volume 5, 6-13 March 2004 Page(s):3414 - 3423 Vol.5
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4. **A novel mobility model and resource reservation strategy for multimedia networks**
Hoang Nam Nguyen; Olariu, S.; Todorova, P.;
[Wireless Communications and Networking Conference, 2002. WCNC2002. 2002. Volume 2, 17-21 March 2002](#) Page(s):832 - 836 vol.2
Digital Object Identifier 10.1109/WCNC.2002.993376
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5. **QoS analysis in mobile cellular networks supporting voice and class of service**
Mahdavi, M.; Edwards, R.M.; Ivey, P.A.;
[Vehicular Technology Conference, 2001. VTC 2001 Fall. IEEE VTS 54th](#)
Volume 4, 7-11 Oct. 2001 Page(s):2692 - 2697 vol.4

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tional from real-time to **non-real-time**. NHRTs typi- cally use the write (real-time) operation;

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automatically by a **real-time clock** when a given. timing constraint; called AAC

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... existing practice and other (**non-real-time**) requirements for a standard clock ... It is

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... and because they occur at unpredictable points in time, **non-real-time** ... This test was

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they occur at unpredictable points in time, **non-real-time**. interrupts can cause significant ...

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... of a 120 MHz Pentium with Linux kernel 2.0.29 (standard or **Non Real-Time** Linux), ... In

RT-Linux (see code) the provided **real time clock** is used to ...

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